

File No.: 100223A001


Date : 2010-04-06

Specifications

Model Number	RP-MR500
Part Number	AT-MR500
Specification.	IEEE802.15.4 RF Module
Date	April 06. 2010
Remarks	

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REVISION HISTORY

MODEL	RP-MR500	Part No.	AT-MR500	PAGE	1
NO	REASON	DESCRIPTION OF CHANGES	REMARKS	DATE	
1	New	RP-MR500 data sheet first releases	Ver.1.0	2010-02-23	
2	Rev1	- Soder mask opening guide added(3Page) - Examples of pcb GND pattern added(4Page) - Compatibility of M100 Series table(13Page)	Ver.1.1	2010-04-06	
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1.1 Scope

This specification is applied to IEEE802.15.4 ZigBee Transceiver Module

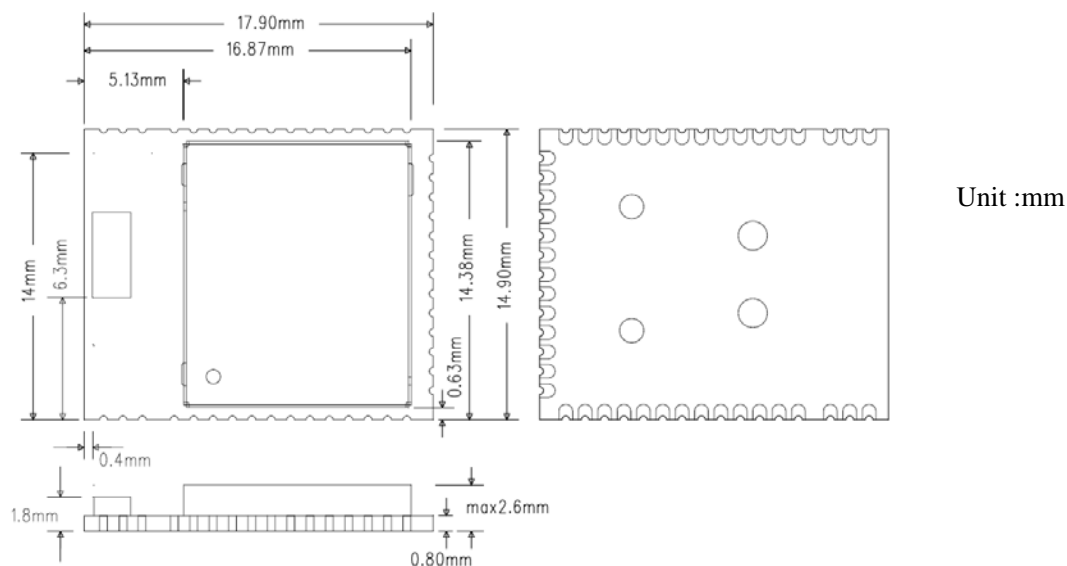
This module has Chip Antenna and embeds 16MHz X-TAL and Single chip SOC.

1.2 Description

Item	Description
Application	Transceiver Module
Frequency Range	2.4 ~ 2.4835 GHz
Technical Standard	IEEE802.15.4
Type	SMD Type
Size	17.9 x 14.9 x 2.6 mm

1.3 Drawing

1) Out line



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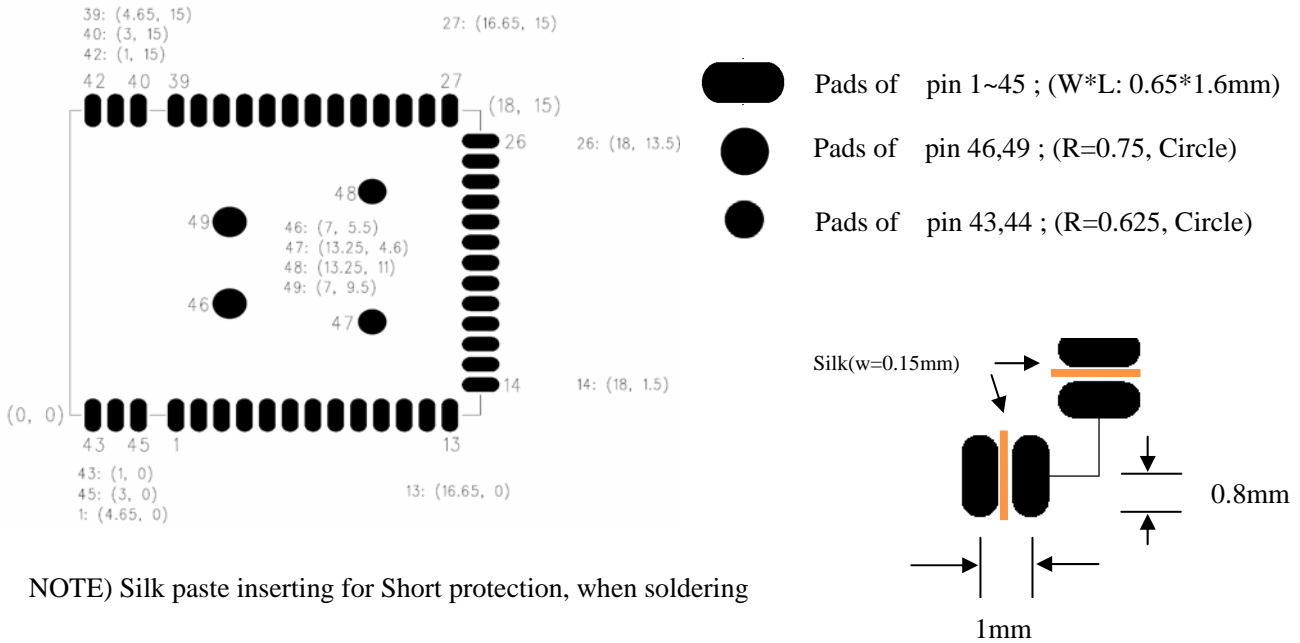
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2) Pcb drawing (top view).



NOTE) Silk paste inserting for Short protection, when soldering

3) Soldermask opening guide

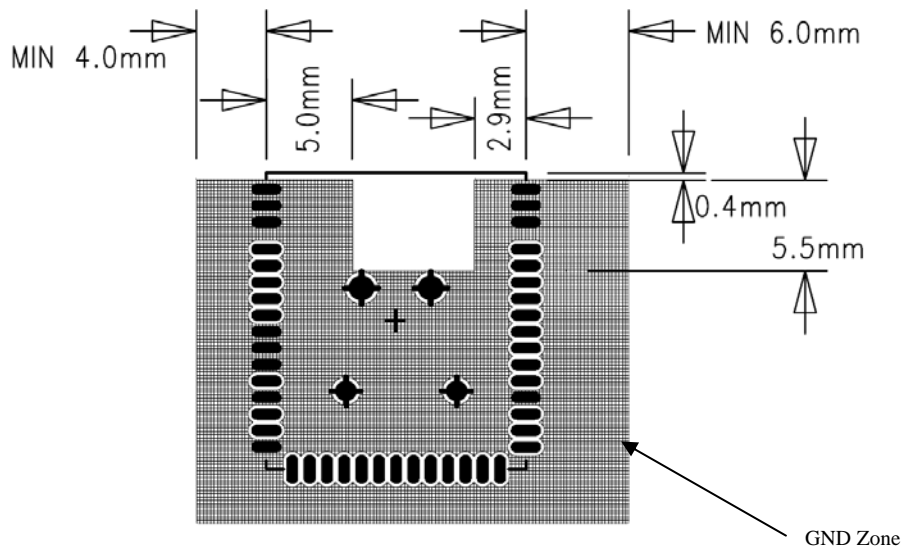
PAD TYPE	PAD SIZE	MASK open	RESULT
	W: 0.65mm L: 1.60mm	W:0.615mm L:1.76~1.8mm	W: About 95% L: About 110~115%
	R=0.75mm	R=0.35mm	46.7%
	R=0.625mm	R=0.25mm	40%

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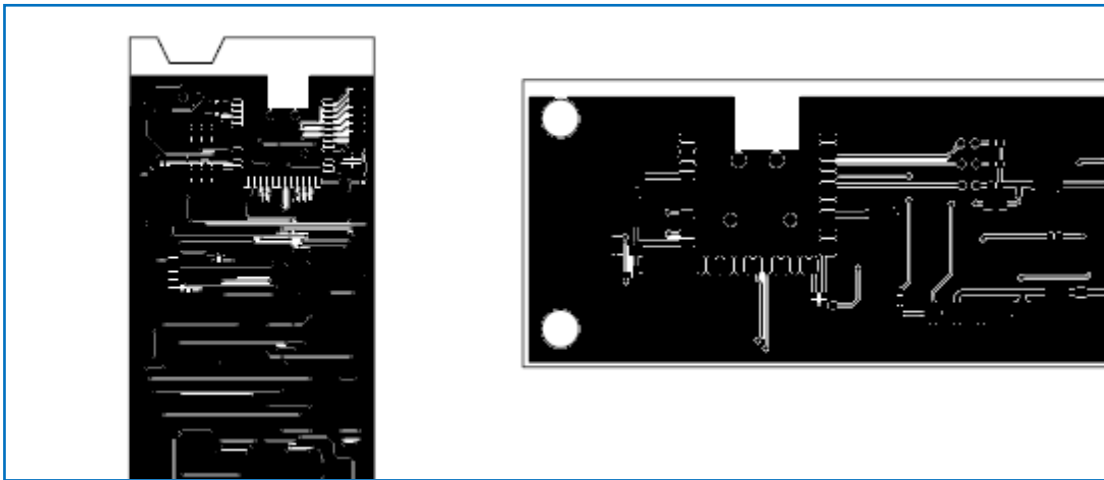
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4) Copper pour Rules of Ground for antenna matching(Absolutely)



Example) Application examples pcb drawing pattern



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1.4 Absolute maximum ratings

Symbol	Parameter	Rating	Unit
V_{DD}	Chip core supply voltage	-0.3 to 1.65	V
3V_IN	I/O supply voltage	-0.3 to 3.6	V
RF _{IN}	Input RF level	10	dBm
T _{STG}	Storage Temperature	-40 to 85	°C

1.5 DC Characteristics

Symbol	Parameter	Min	Typ.	Max	Unit
V_{DD}	Chip core supply voltage (AVDD_1.5V,DVDD_1.5V)	1.35	1.5	1.65	V
3V_IN	I/O supply voltage(V _{DDIO})	1.35(*)	3.0	3.3	V
V _{IH}	High level input voltage	0.7x V _{DDIO}		V _{DDIO}	V
V _{IL}	Low level input voltage	0		0.3x V _{DDIO}	V
V _{OH}	High level output voltage	V _{DDIO} -0.5		V _{DDIO}	V
V _{OL}	Low level output voltage	0		0.4	V
T _A	Air temperature	-40		85	°C

(*) : MSV="L", min 1.9V(regulator drop voltage), 3VIN=2.7~3.3V is Recommended.

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1.6 Electrical specifications

(Condition: EVM Board , at 25℃, 3V_IN=3.0V, VDD(AVDD,DVDD=1.5V,MSV="H")

Parameter	Min	Typ.	Max	Unit
Current consumption				
Active MCU without RX/TX operation (AES, Peripheral, SADC Disabled)		4.6		mA
Active MCU with TX Mode (AES, Peripheral, SADC Disabled)				
@+8dBm output power		45.1		mA
@+7dBm output power		43.2		
@+6dBm output power		41.5		
@+5dBm output power		41.4		
@+4dBm output power		37.8		
@+3dBm output power		36.2		
@+2dBm output power		34.8		
@+1dBm output power		33.9		
@+0dBm output power		32.7		
Active MCU with RX Mode (AES, Peripheral, SADC Disabled)		35.2		mA
PM1		25		uA
PM2		1.7		uA
PM3		0.3 ⁹		uA
AES		3.1		mA
Peripheral		2.6		mA
Sensor ADC		1		mA


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<table border="1"> <thead> <tr> <th>Parameter</th> <th>Min</th> <th>Typ</th> <th>Max</th> <th>Unit</th> </tr> </thead> <tbody> <tr> <td colspan="5">RF Characteristics</td> </tr> <tr> <td>RF Frequency Range</td> <td>2.4</td> <td></td> <td>2.4835</td> <td>GHz</td> </tr> <tr> <td>Transmit data rate(normal mode)</td> <td></td> <td>250</td> <td></td> <td>kbps</td> </tr> <tr> <td>Transmit data rate(turbo mode)</td> <td></td> <td>500</td> <td></td> <td>kbps</td> </tr> <tr> <td>Transmit data rate(premium mode)</td> <td></td> <td>1000</td> <td></td> <td>kbps</td> </tr> <tr> <td>Transmit chip rate</td> <td></td> <td>2000</td> <td></td> <td>kbps</td> </tr> <tr> <td>Maximum output power</td> <td></td> <td></td> <td>8</td> <td>dBm</td> </tr> <tr> <td>Programmable output power range</td> <td></td> <td>30</td> <td></td> <td>dB</td> </tr> <tr> <td>Receiver sensitivity</td> <td></td> <td></td> <td></td> <td></td> </tr> <tr> <td> Normal mode</td> <td></td> <td>-98</td> <td></td> <td>dBm</td> </tr> <tr> <td> Turbo mode</td> <td></td> <td>-95</td> <td></td> <td></td> </tr> <tr> <td> Premium mode</td> <td></td> <td>-91</td> <td></td> <td></td> </tr> <tr> <td>Adjacent Channel Rejection</td> <td></td> <td></td> <td></td> <td></td> </tr> <tr> <td> +5MHz</td> <td></td> <td>49</td> <td></td> <td>dBc</td> </tr> <tr> <td> -5MHz</td> <td></td> <td>48.8</td> <td></td> <td></td> </tr> <tr> <td>Alternate Channel Rejection</td> <td></td> <td></td> <td></td> <td></td> </tr> <tr> <td> +10MHz</td> <td></td> <td>56.1</td> <td></td> <td>dBc</td> </tr> <tr> <td> -10MHz</td> <td></td> <td>56.8</td> <td></td> <td></td> </tr> <tr> <td>Co-Channel Rejection</td> <td></td> <td>-10.7</td> <td></td> <td>dBc</td> </tr> <tr> <td>Blocking/Desensitization</td> <td></td> <td></td> <td></td> <td></td> </tr> <tr> <td> +/- 5 MHz</td> <td></td> <td>-45</td> <td></td> <td>dBm</td> </tr> <tr> <td> +/- 10 MHz</td> <td></td> <td>-42</td> <td></td> <td></td> </tr> <tr> <td> +/- 15 MHz</td> <td></td> <td>-48</td> <td></td> <td></td> </tr> <tr> <td> +/- 20 MHz</td> <td></td> <td>-40</td> <td></td> <td></td> </tr> <tr> <td> +/- 30 MHz</td> <td></td> <td>-43</td> <td></td> <td></td> </tr> <tr> <td> +/- 50 MHz</td> <td></td> <td>-46</td> <td></td> <td></td> </tr> </tbody> </table>						Parameter	Min	Typ	Max	Unit	RF Characteristics					RF Frequency Range	2.4		2.4835	GHz	Transmit data rate(normal mode)		250		kbps	Transmit data rate(turbo mode)		500		kbps	Transmit data rate(premium mode)		1000		kbps	Transmit chip rate		2000		kbps	Maximum output power			8	dBm	Programmable output power range		30		dB	Receiver sensitivity					Normal mode		-98		dBm	Turbo mode		-95			Premium mode		-91			Adjacent Channel Rejection					+5MHz		49		dBc	-5MHz		48.8			Alternate Channel Rejection					+10MHz		56.1		dBc	-10MHz		56.8			Co-Channel Rejection		-10.7		dBc	Blocking/Desensitization					+/- 5 MHz		-45		dBm	+/- 10 MHz		-42			+/- 15 MHz		-48			+/- 20 MHz		-40			+/- 30 MHz		-43			+/- 50 MHz		-46		
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Parameter	Min	Typ	Max	Unit
Spurious Emission(30Hz~1GHz)		-60		dBm
Spurious Emission(1GHz~2.5GHz)		-40		dBm
Spurious Emission(2.5GHz~12.7GHz)		-50		dBm
2 nd Harmonics		-50		dBm
3 rd Harmonics		-70		dBm
Frequency Error Tolerance			±200	KHz
Error Vector Magnitude(EVM)		9.8		%
Saturation(Maximum Input Level)		5		dBm
RSSI Dynamic Range		90		dB
RSSI Accuracy		±1.2	+6/-3	dB
RSSI Linearity		±0.2	±6	dB
RSSI Average Time		128		uSEC
Symbol Rate Error Tolerance		TBD		ppm
Optimum Load Impedance		TBD		Ω

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Parameter	Min	Typ	Max	Unit
Frequency Synthesizer				
Phase Noise				
@ ±100KHz offset		-80.3		dBc/Hz
@ ±1MHz offset		-108.8		
@ ±2MHz offset		-113.3		
@ ±3MHz offset		-120.2		
@ ±5MHz offset		-124.2		
PLL Lock Time		110		uSEC
PLL Jitter		16		Psec
Crystal Frequency Accuracy Requirement	-20		+20	ppm
ESR		TBD		Ω
Recommend C ₀		TBD		pF
Recommend C _L		TBD		pF
On-chip RC Regulator				
Frequency		32.78		KHz
Frequency Accuracy		TBD		ppm
Sensor ADC				
Number of Bits		8		bits
Conversion Time		256		uSEC

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Parameter	Min	Typ	Max	Unit
Differential Nonlinearity(DNL)		±1.7		LSB
Integral Nonlinearity(INL)		±2.4		LSB
SINAD(Sine Input)		51.0		dB
On-Chip Voltage Regulator				
Supply range for Regulator	1.9	3.0	3.6	V
Regulated Output		1.5		V
Maximum Current			140	mA
No Load Current		15		uA
Start-up Time		260		uSEC

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1.7 Electrical Interface

Terminal	NAME	Inter face	I/O	Description
1	ACH0	Analog	I/O	Sensor ADC input
2	ACH1	Analog	I/O	Sensor ADC input
3	ACH2	Analog	I/O	Sensor ADC input
4	ACH3	Analog	I/O	Sensor ADC input
5	AVDD_1.5V	Power	I/O	1.5V Power Supply input/output
6	AGND	Ground	-	RF Ground
7	MS0	Digital	I	Mode select
8	MS1	Digital	I	Mode select
9	MS2	Digital	I	Mode select
10	MSV	Digital	I	Mode select of voltage(0=1.5V)
11	RESETB	Digital	I	Reset (Active Low)
12	3V_IN	Power	I	3V Power supply
13	DGND	Ground	-	Ground for digital core and I/O
14	P1[7]	Digital	O	Port P1.7GPO/P0AND/TRSW/Fold/Clock/BIST Fail Indicator
15	P1[6]	Digital	B	Port P1.6/TRSWB
16	P1[5]	Digital	B	Port P1.5
17	P1[4]	Digital	B	Port P1.4 /QUADZB/Sleep Timer OSC Buffer Input.
18	P1[3]	Digital	B	Port P1.3/QUADZA/Sleep Timer OSC Buffer Output/RTCLKOUT
19	P1[2]	Digital	B	Port P1.2
20	P1[1]	Digital	B	Port P1.1/TXD1
21	P1[0]	Digital	B	Port P1.0/RXD1
22	P3[7]	Digital	B	Port P3.7/12mA Drive capability /PWM3/CTS1/SPICSN(slave only)
23	P3[6]	Digital	B	Port P3.6/12 mA Drive capability /PWM2/RTS1/SPICLK



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Terminal	NAME	Inter face	I/O	Description
24	P3[5]	Digital	B	Port P3.5/T1/CTS0/QUADYB/SPIDO
25	P3[4]	Digital	B	Port P3.4/T0/RTS0/QUADYA/SPIDI
26	P3[3]	Digital	B	Port P3.3/INT1(active low)
27	P3[2]	Digital	B	Port P3.2/INT0(active low)
28	P3[1]	Digital	B	Port P3.1/TXD0/QUADXB
29	P3[0]	Digital	B	Port P3.0/RXD0/QUADXA
30	DGND	Ground	-	Ground for digital core and I/O
31	DVDD_1.5V	Power	I/O	1.5V Power Supply input/output
32	P0[7]	Digital	B	Port P0.7/I2STX_MCLK
33	P0[6]	Digital	B	Port P0.6/I2STX_BCLK
34	P0[5]	Digital	B	Port P0.5/I2STX_LRCK
35	P0[4]	Digital	B	Port P0.4/I2STX_DO
36	P0[3]	Digital	B	Port P0.3/I2SRX_MCLK
37	P0[2]	Digital	B	Port P0.2/I2SRX_BCLK
38	P0[1]	Digital	B	Port P0.1/I2SRX_LRCK
39	P0[0]	Digital	B	Port P0.0/I2SRX_DI
40,41,42	AGND	Ground	-	RF Ground
43,44,45	AGND	Ground	-	RF Ground
46,49	AGND	Ground	-	RF Ground
47,48	DGND	Ground	-	Ground for digital core and I/O

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1.8 Compatibility of M100 Series table.

MR500	M100	NAME	MR500	M100	NAME
1	1	ACH0	26	26	P3[3]
2	2	ACH1	27	27	P3[2]
3	3	ACH2	28	28	P3[1]
4	4	ACH3	29	29	P3[0]
5	5	AVDD_1.5V	30	30	DGND
6	6	AGND	31	31	DVDD_1.5V
7	7	MS0	32	32	P0[7]
8	8	MS1	33	33	P0[6]
9	9	MS2	34	34	P0[5]
10	10	MSV	35	35	P0[4]
11	11	RESETB	36	36	P0[3]
12	12	3V_IN	37	37	P0[2]
13	13	DGND	38	38	P0[1]
14	14	P1[7]	39	39	P0[0]
15	15	P1[6]	40	-	AGND
16	16	P1[5]	41	-	AGND
17	17	P1[4]	42	-	AGND
18	18	P1[3]	43	-	AGND
19	19	P1[2]	44	-	AGND
20	20	P1[1]	45	-	AGND
21	21	P1[0]	46	43	AGND
22	22	P3[7]	47	44	DGND
23	23	P3[6]	48	45	DGND
24	24	P3[5]	49	46	AGND
25	25	P3[4]		40,41	NC



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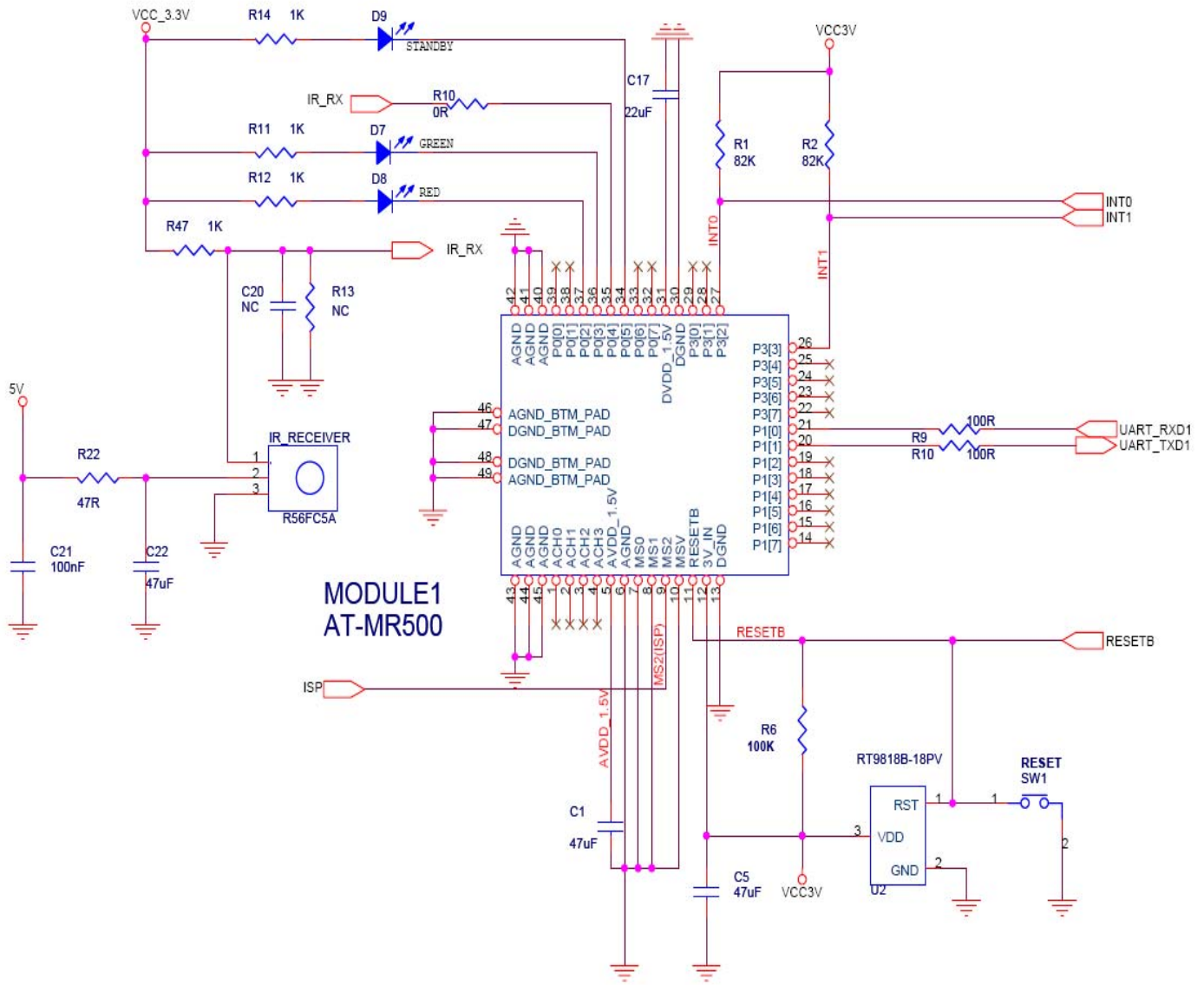
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2. Schematic for applications

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2. Schematic for Applications



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3. Realiability & Antenna Specification

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3.1 Reliability

No.	Test item	Test condition
1	Reflow Thermal Cycle	Normal Pbfree reflow Condition.2 times
2	Thermal Shock Cycle	30min. at -40℃, 30min. at 85℃ , 100Cycles Recovery Time 2hours
3	Vibration Test	50Hz -> 500Hz -> 50Hz , 15min/Cycle X,Y,Z : Each 12 times [Total : 9hours]
4	High Temperature Storage Test	96 hours at 85℃±2℃, Recovery Time 2hours
5	Low Temperature Storage Test	96 hours at -40℃±2℃, Recovery Time 2hours
6	High Temperature & Humidity Storage Test	96 hours at 60℃±2℃ & 95%RH±2%RH. Recovery Time 2hours
7	Operating Temperature TEST	96hours at -40℃,85℃
8	High Temperature & Humidity Operating Test	24hours at 60℃ & 85%±2%RH.
9	Drop Test	Height min 76 cm, All sides onto Iron plate(T=min2mm).

3.2 Specification of Antenna

ITEM	UNIT	MIN	TYP	MAX
Frequency	MHz	2400		2500
V.S.W.R				3.0
Gain	dBi		1.4	
Maximum input power	W			1
Characteristics TYPE	Don't care			
Polarization	Vertical			
Radiated Pattern	Omni-directional			
Impedence	50			
SIZE	3.2X1.56X1.1tmm			



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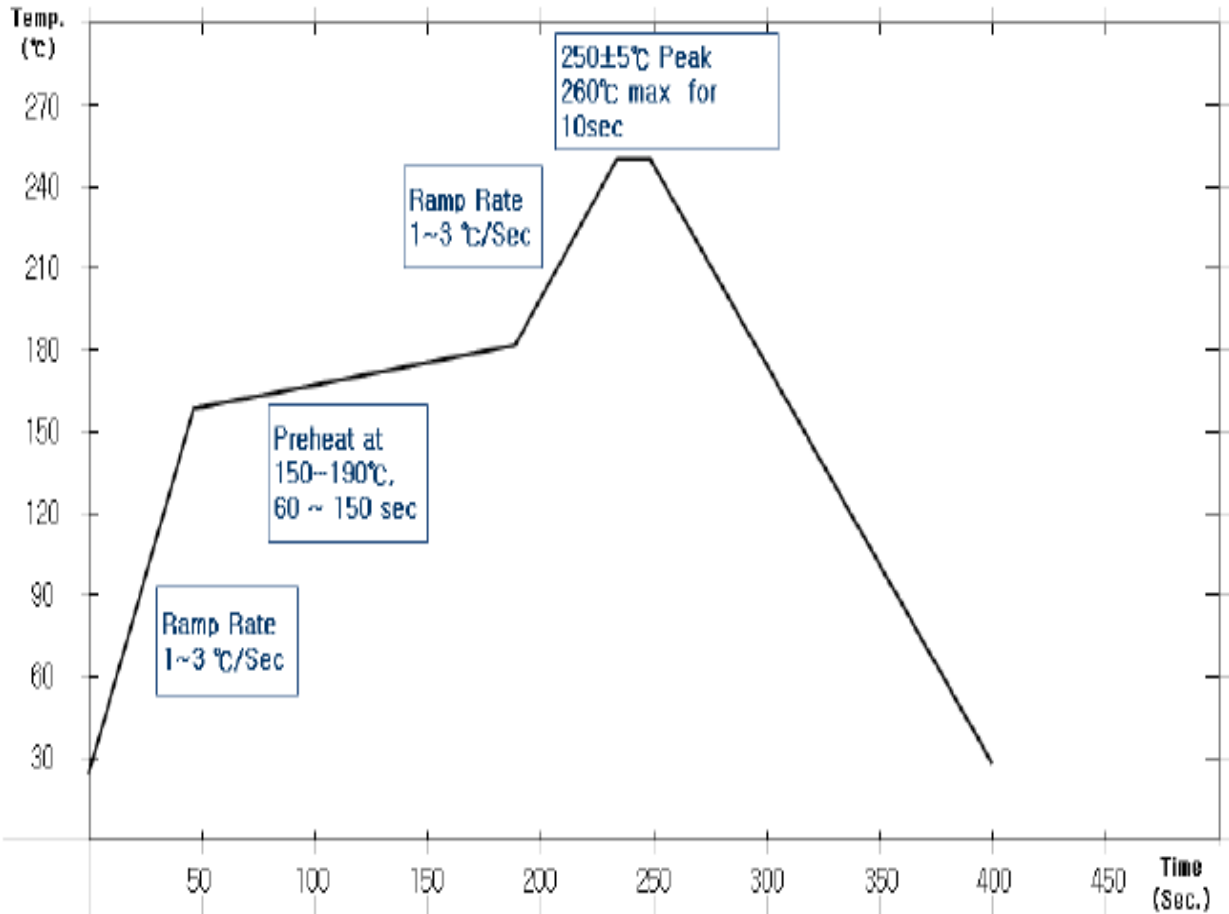
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4. Reflow Profile

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4. Reflow Profile

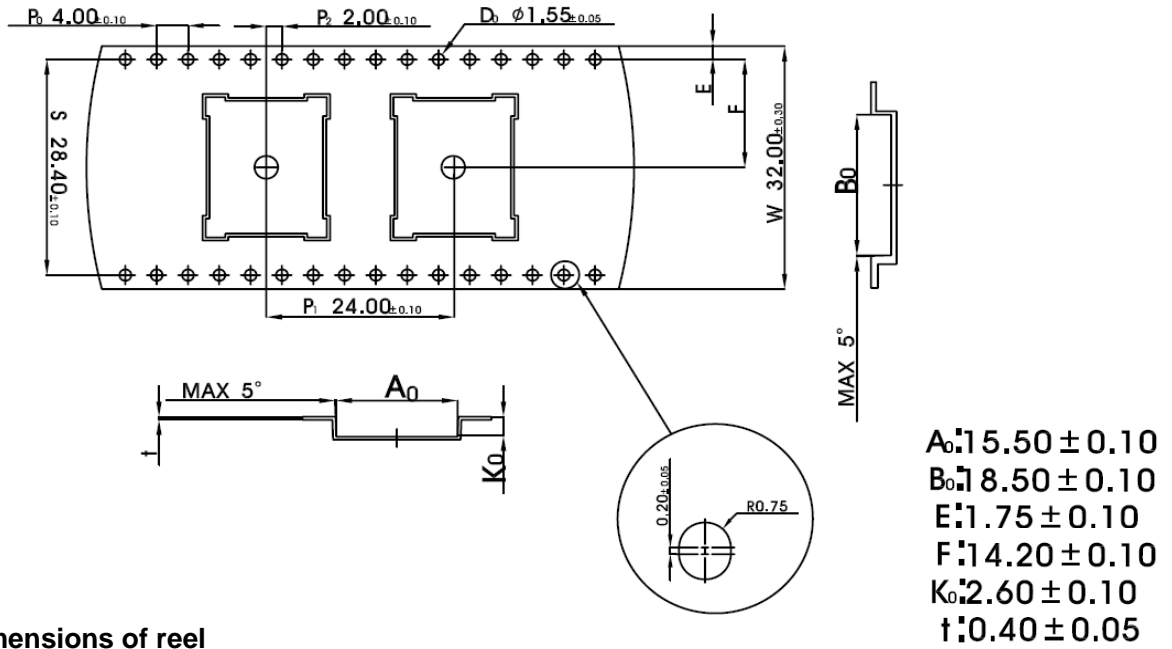


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	S.B.LEE	S.S.PARK	A.K.LEE
	02/23/2010	02/23/2010	02/23/2010

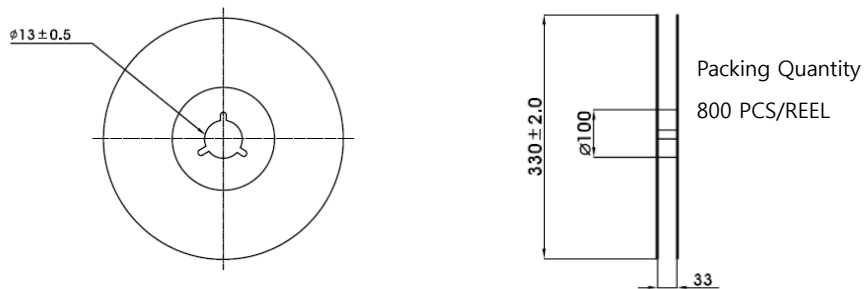
5. Package

MODEL	RP-MR500	Part No.	AT-MR500	PAGE	17
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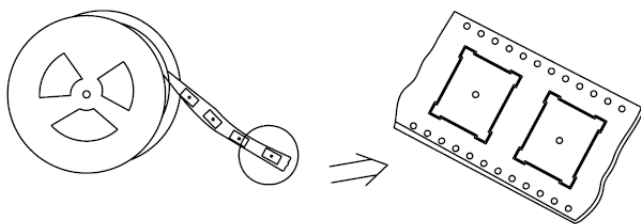
5.1 Dimensions of tape



5.2 Dimensions of reel



5.3 Taping style

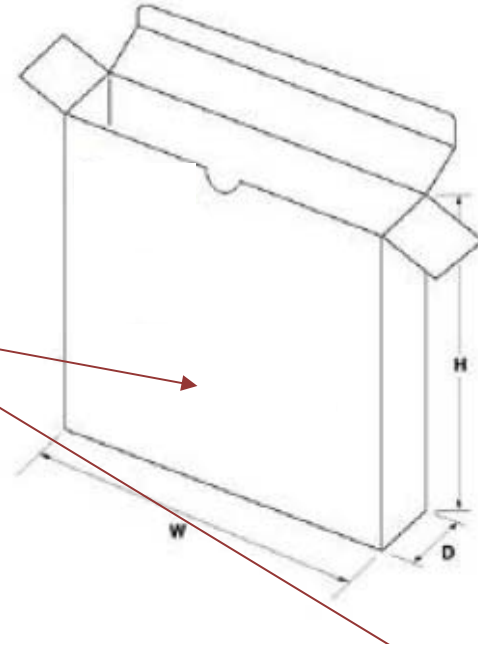
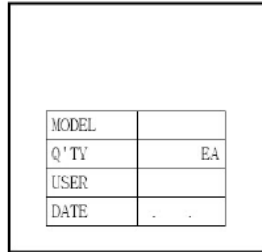


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5. Package

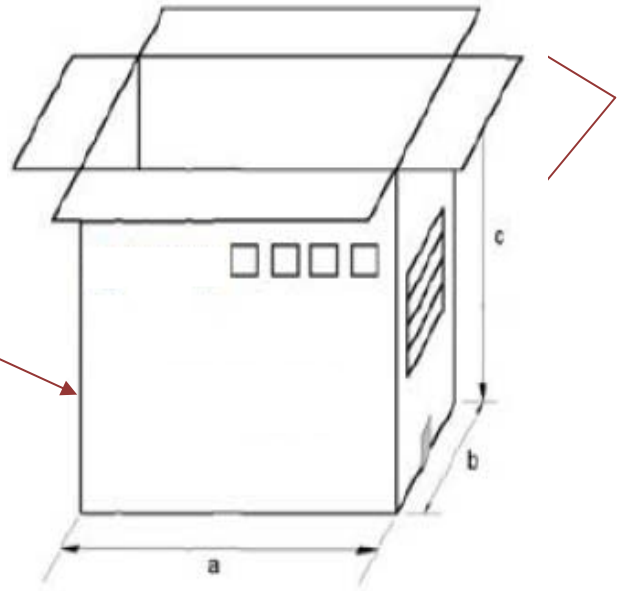
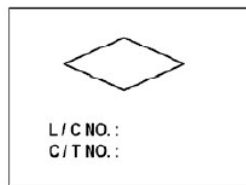
MODEL	RP-MR500	Part No.	AT-MR500	PAGE	18
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5.4 Dimensions of inner BOX (MOQ : 800EA/1REEL)




	W	D	H
Spec.	335	45	335

5.5 Dimensions of out BOX (MOQ : 5600EA/1BOX, 7REEL/1BOX)



	a	b	c
Spec.	335	335	335

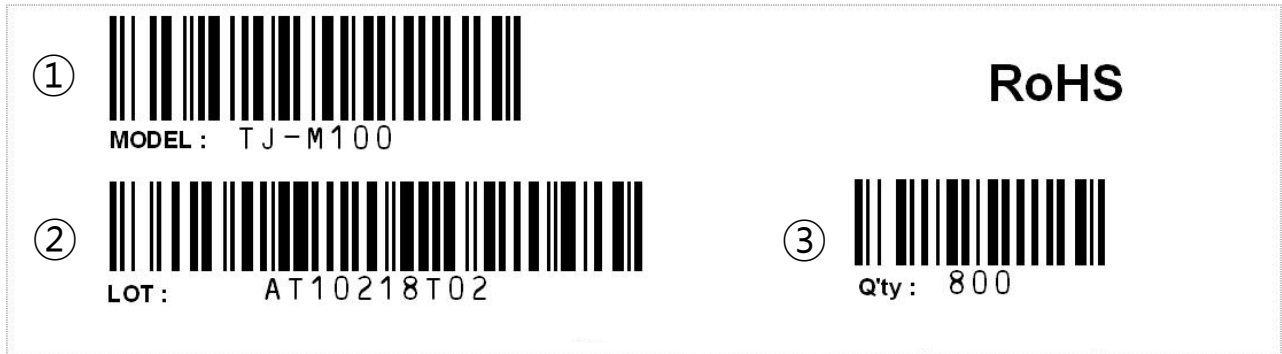
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5. Package

MODEL	RP-MR500	Part No.	AT-MR500	PAGE	19
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5.5 Bar Code Label

[Reel & Small Carton Box Bar Code Label] 36mm X 100mm



① : Model name(Part Number)

② : Lot Number


AT : Vender first name

10218 : 2010, FEB.18

T02 : Run Number(T02:SN 00801~01600)

(800EA/1run)

③ : Quantity

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